

Cmpware, Inc.

Tools for multicore multiprocessing

The **Cmpware Configurable Multiprocessor Development Kit (CMP-DK)** is a powerful development environment for architectural exploration and application programming of multicore and multiprocessor systems.

Features include:

- Powerful *Eclipse* based IDE
- Fast processor simulation (**25M+** cycles/sec)
- Simple processor definition
- Configurable / user-definable networks
- Built in profiling and performance monitor
- Fast access to all system status
- Easy custom hardware model definition
- Standard processor models for *MIPS32*, *SPARC-8 (LEON)*, *PowerPC*, *ARC 700*, *Tensilica Extensa*, *Xilinx MicroBlaze*, *Altera NIOS*, *LatticeMicro32* and *Cell BE*.

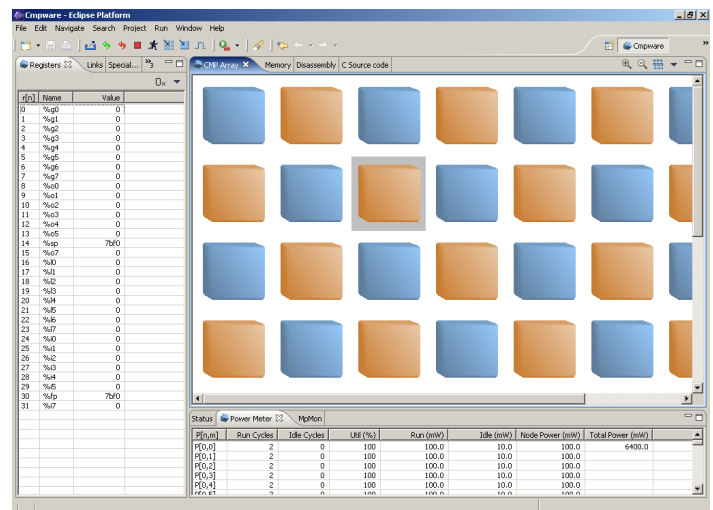
Because the *Cmpware* toolkit is truly platform independent, it permits easy user specification of the processing nodes as well as the interconnection network. This permits the *Cmpware* toolkit to be used for a wide variety of standard and custom platforms.

Now you can define the multiprocessor architecture, program and simulate it all in one unified Integrated Development Environment.

Modern Multicore / Multiprocessor Design

Increasingly, systems are turning to multiple processor cores to reach power and performance goals. Such multiprocessor implementations are turning up in desktop microprocessors, custom Application Specific

Integrated Circuits (ASICs) and even in Field Programmable Gate Array (FPGA) design.



Such systems offer enormous benefits, particularly in their ability to be reprogrammed to do a variety of tasks. The *Cmpware* toolkit provides a powerful environment for the programming and modeling of multicore and multiprocessor architectures, giving previously unavailable visibility into the details of the system state and performance. Such visibility into the internal working of a multiprocessor device is invaluable in designing, analyzing, programming and debugging such systems.

The *Cmpware* toolkit is fully extensible and can easily support a wide variety of processors, interconnection networks and even custom hardware modules.

If you are still trying to design the hardware of tomorrow with yesterday's tools, give *Cmpware* a look. We think you'll like what you see.

